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ABSTRACT

A method of encoding at least one input bit set of ordered bits with permutation position integers comprising encoding the input bit set using a first encoder having a multi-state register to provide a first output; selectively reordering the input bit set using an interleaver to provide a reordered input bit set; and encoding the reordered input bit set using a second encoder having a multi-state register to provide a second output; whereby the value of said second encoder register is the same as the value of said first encoder register upon completion of the procedure.

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